V Dikshita Rao

Roll No.: 2200040263

B.Tech in Electronics & Communication

KL University, Vijayawada

📞 +91-9630208337

✉ 2200040263ece@gmail.com

✉ dikshitarao06@gmail.com

🔗 LinkedIn Profile

1 Profile

Tech enthusiast specializing in VLSI and Software Development, with a strong foundation in front-end engineering and AI-driven applications. Pursuing a B.Tech in Electronics and Communication Engineering, passionate about building scalable solutions and optimizing user experiences. Seeking opportunities in top-tier organizations to contribute to semiconductor innovation and software-driven advancements.

2 Education

- **KL UNIVERSITY** — *2022-2026*
  B.Tech in Electronics and Communication — CGPA: 9.25

- **BSP- Senior Secondary School Sec-X** — *2019-2022*
  CBSE-12th — Percentage: 89

- **BSP- Senior Secondary School Sec-X** — *2018-2019*
  CBSE-10th — Percentage: 94

3 Experience

- **BHILAI STEEL PLANT (SAIL)** — *May 2024*
  Industrial Intern — BHILAI, Chhattisgarh

  - Interned at BSP exploring Instrumentation, Blast Furnace, Steel Melting Shop, Rail Mill, Incos, and C&IT
  - Gained hands-on experience in industrial automation, steel production, and quality control
  - Enhanced technical knowledge working alongside experienced industry professionals

4 Personal Projects

- **Book Recommendation System** — *Dec 2023*

  - Developed Book Recommendation System using Python, implementing API integration for data access
  - Built automation algorithms to process and analyze large datasets from Kaggle
  - Utilized Python libraries (Pandas, NumPy, Scikit-learn) for AI-based filtering and personalization
  - Created an automated user interface in PyCharm with intelligent preference learning capabilities

- **SRAM Cell Design using 45nm Technology** — *May 2024*

  - Designed and simulated 6T SRAM cell using 45nm CMOS technology in Cadence Virtuoso.
  - Performed schematic design, layout, and verification using Virtuoso, Spectre, and Assura tools
  - Analyzed stability, power, and delay through read/write operations and SNM evaluation ment.

- **Traffic Congestion Control System** — *Dec 2024*

  - Developed IoT-based traffic control system using Arduino Uno and sensors
  - Integrated hardware and software for real-time congestion detection
  - Optimized signal adjustments for improved traffic flow

5 Technical Skills

Languages: C, Java,HTML,CSS,Verilog HDL,Python
Skills:Strong Communication,Problem-Solving,Teamwork Collaboration,Time Management,Adaptability
Interests: VLSI Design Verification,Digital Circuit Design,IoT,Machine Learning ,Front-End Development,EDA Tools

6 Certifications

- **Salesforce AI-Associate:** Experienced Salesforce AI Associate for intelligent business solutions

- **Embedded Systems and IIOT:** L1 and L2 by TESSOLVE

- **SYSTEM VERILOG and UVM:** Advanced digital design and test environment development

7 Positions & Responsibilities

- **Position,Core Member** - *Always@VLSI Club* — *2023*

- **Position, PR Team and Hospitality** - *KL Kutumb,North Indian Society* — *2022 - Present*